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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/775,441	02/10/2004	Rama Divakaruni	FIS920000337US3 (14114Z)	9423
23389 75	590 02/09/2006		EXAM	INER
SCULLY SCOTT MURPHY & PRESSER, PC 400 GARDEN CITY PLAZA			LOKE, STEVEN HO YIN	
SUITE 300			ART UNIT	PAPER NUMBER
GARDEN CIT	Y, NY 11530		2811	

DATE MAILED: 02/09/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

			al
	Application No.	Applicant(s)	
	10/775,441	DIVAKARUNI ET AL.	
Office Action Summary	Examiner	Art Unit	
	Steven Loke	2811	
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet wi	h the correspondence address	
A SHORTENED STATUTORY PERIOD FOR REPL' WHICHEVER IS LONGER, FROM THE MAILING D Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication If NO period for reply is specified above, the maximum statutory period of Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNIC 36(a). In no event, however, may a re will apply and will expire SIX (6) MON' e, cause the application to become AB.	CATION. ply be timely filed IHS from the mailing date of this communication. ANDONED (35 U.S.C. § 133).	
Status			
1)⊠. Responsive to communication(s) filed on 16 N	lovember 2005.		
	action is non-final.		
3) Since this application is in condition for allowa	1	ers, prosecution as to the merits is	
closed in accordance with the practice under E		· ·	
Disposition of Claims			
4) ⊠ Claim(s) <u>1-4</u> is/are pending in the application. 4a) Of the above claim(s) is/are withdray 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) <u>1-4</u> is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction and/o		. •	
Application Papers			
9) The specification is objected to by the Examine	er.		
10)☐ The drawing(s) filed on is/are: a)☐ acc	epted or b) Objected to b	y the Examiner.	
Applicant may not request that any objection to the	drawing(s) be held in abeyan	ce. See 37 CFR 1.85(a).	
Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Ex	,		
Priority under 35 U.S.C. § 119			
 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Bureau 	s have been received. s have been received in Aprity documents have been	oplication No	
* See the attached detailed Office action for a list	of the certified copies not	eceived.	
	•	•	
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Attachment(s)	_		
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 	Paper No(s	ummary (PTO-413) /Mail Date formal Patent Application (PTO-152) 	

Art Unit: 2811

1. Claims 1-4 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

Fig. 2G and 2H disclose the side portions of the trench top oxide (TTO) layer [28] located inside the deep trench extending upwardly along portions of the vertical walls of the deep trench. The specification never discloses the trench top oxide (TTO) layer located inside the deep trench without extending upwardly along vertical walls of the deep trench as claimed in claim 1.

Fig. 2G and 2H disclose each said vertical MOSFET includes a gate dielectric [70] formed on an inner surface of a sidewall of each said deep trench. The specification never discloses each said vertical MOSFET includes gate dielectrics formed on inner surfaces of sidewalls of each said deep trench as claimed in claim 3.

2. Claims 1-4 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 1, lines 8-9, the phrase "the collar oxide region formed on a remaining wall portion of each deep trench not containing said buried-strap out diffusion region" is vague and indefinite. Figs. 2G and 2H disclose the collar oxide [18] formed on the right side and the lower left side of the trench. The figures further disclose the nitride liner [50] and the gate oxide [70] formed on the upper left side of the trench. Since there is a

vertical MOSFET in the device, at least a portion of the sidewall of the trench adjacent the gate oxide is not containing the buried-strap out diffusion region. Therefore, it is believed that the collar oxide region is formed on the right side and lower left side of the wall portion of each deep trench not containing said buried-strap out diffusion region.

3. Applicant's arguments with respect to claims 1-4 have been considered but are moot in view of the new ground(s) of rejection.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven Loke whose telephone number is (571) 272-1657. The examiner can normally be reached on 8:00 am to 5:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Lee can be reached on (571) 272-1732. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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February 5, 2006

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Steven Loke Primary Examiner

Steven Loke